

IIIT Hyderabad LEEE Exam Pattern

Mode of Exam	Computer-Based Test
Exam Duration	3 hours (180 minutes)
Types of Questions	Objective-Based Test (MCQ)
Subject Names	CSE - General Aptitud, Discrete Mathematics, Digital Logic, Computer Organization, Data Structures, C-Programming. ECE - General Aptitude, Digital Logic, Circuit Theory and Networks, Electronic Devices and Circuits, Signals and Systems.

IIIT Hyderabad LEEE Syllabus

Computer Science and Engineering Exam

IIIT Hyderabad LEEE CSE Syllabus	
General Aptitude	<ul style="list-style-type: none">● Verbal Aptitude - Basic English grammar, Tenses, articles, adjectives, prepositions, conjunctions, verb-noun agreement, and other parts of speech Basic vocabulary: words, idioms, and phrases in context Reading and comprehension Narrative sequencing● Data interpretation - Data graphs (bar graphs, pie charts, and other graphs representing data), 2- and 3-dimensional plots, maps, and tables Numerical computation and estimation: ratios, percentages, powers, exponents and logarithms, permutations and combinations, and series Mensuration and geometry Elementary statistics and probability● Analytical Aptitude - Logic deduction and induction, Analogy Numerical relations and reasoning● Spatial Aptitude - Transformation of shapes: translation, rotation, scaling, mirroring, assembling, and grouping Paper folding, cutting, and patterns in 2 and 3 dimensions
Discrete Mathematics	<ul style="list-style-type: none">● Algorithm● Binary● Discrete Mathematics● Logic

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Digital Logic	<ul style="list-style-type: none">● Number System and Boolean Algebra● Minimization Techniques● Combinational Circuits● Sequential Circuits● Memory Devices
Computer Organization	<ul style="list-style-type: none">● Basic Computer Instructions● Instruction Design and Format● Computer Arithmetic● Microprogrammed Control● Memory Organization
Data Structures	<ul style="list-style-type: none">● Array● Pointer● Structure● Linked List● Stack● Queue● Graph● Searching● Sorting● Programs

C-Programming	<ul style="list-style-type: none">• Arrays• Conditional statements and switches• File handling• Variables and handling datatypes• Structures• Functions• Pointers• System-level operations using inbuilt headers• Building functions of Strings
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Electronics and Communications Engineering Exam

IIIT Hyderabad LEEE ECE Syllabus	
General Aptitude	<ul style="list-style-type: none">• Verbal Aptitude - Basic English grammar, Tenses, articles, adjectives, prepositions, conjunctions, verb-noun agreement, and other parts of speech Basic vocabulary: words, idioms, and phrases in context Reading and comprehension Narrative sequencing• Data interpretation - Data graphs (bar graphs, pie charts, and other graphs representing data), 2- and 3-dimensional plots, maps, and tables Numerical computation and estimation: ratios, percentages, powers, exponents and logarithms, permutations and combinations, and series Mensuration and geometry Elementary statistics and probability• Analytical Aptitude - Logic deduction and induction, Analogy Numerical relations and reasoning• Spatial Aptitude - Transformation of shapes: translation, rotation, scaling, mirroring, assembling, and grouping Paper folding, cutting, and patterns in 2 and 3 dimensions

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Digital Logic	<ul style="list-style-type: none">● Number System and Boolean Algebra● Minimization Techniques● Combinational Circuits● Sequential Circuits● Memory Devices
Circuit Theory and Networks	<ul style="list-style-type: none">● Network analysis using KCL and KVL● Laplace transform● Network Theorems● Phasor diagrams● Transient Analysis of First-order circuits● Magnetic Coupled circuits● Resonance● Two-port Networks
Electronic Devices and Circuits	<ul style="list-style-type: none">● Semiconductor diode: I-V characteristics for forward and reverse bias; and diode as a rectifier● I-V characteristics of LED, solar cell, photodiode, and Zener diode; Zener diode used as a voltage regulator● Junction transistor, characteristics of a transistor, transistor action; transistor as an amplifier (common emitter configuration) and oscillator● Logic gates (OR, AND, NOT, NAND and NOR).● Transistor as a switch.● Semiconductors
Signals and Systems	<ul style="list-style-type: none">● Discrete-Time Signals● Fourier Series and its Application● LTI Systems● Representation of Continuous● Discrete-Time Signals and Sampling Theorem

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